**PROTOCOL**to laboratory exercise

***Broad-Band-Amplifier***



|  |  |  |
| --- | --- | --- |
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| ***Transistor***  *2x BC546* | | |
| **Used Devices**   |  |  |  |  |  | | --- | --- | --- | --- | --- | | Nr. | Device | Manufactor | Type | Place Nr. | | 1. | Oscilloscope | Tektronix | TDS 1001B | - | | 2. | Function generator | Hameg | HM 8035 | - | | 3. | Power Supply | PS | 2403D | - |   **Used Programs**   |  |  |  | | --- | --- | --- | | Nr. | Name | Version | | 1. | Altium Designer | 13 | | 2. | Micro-Cap | 11 | | | |

ÜBUNGS-/ABGABE-DATUM

Klasse /Gruppe

NOTE

LEHRER

# Table of Contents

[1 Table of Contents 2](#_Toc405320442)

[2 Tasks 3](#_Toc405320443)

[2.1 Given Values 3](#_Toc405320444)

[3 Dimensioning 3](#_Toc405320445)

[3.1 Calculation R3 3](#_Toc405320446)

[3.2 Calculation R2 4](#_Toc405320447)

[3.3 Calculation R1 4](#_Toc405320448)

[3.4 Calculation C1 und C2 4](#_Toc405320449)

[3.5 Calculation C3 4](#_Toc405320450)

[3.6 Calculation C4 4](#_Toc405320451)

[4 Measuring construction 5](#_Toc405320452)

[5 Bias voltage 6](#_Toc405320453)

[6 Output measurement 7](#_Toc405320454)

[6.1 Dynamic Range 7](#_Toc405320455)

[6.1.1 Measurement results (100mVpp) 8](#_Toc405320456)

[6.1.2 Measurement results (200mVpp) 9](#_Toc405320457)

[6.1.3 Measurement results (300mVpp) 10](#_Toc405320458)

[7 Bode plot 11](#_Toc405320459)

[7.1 Amplitude plot 11](#_Toc405320460)

[7.2 Phase plot 11](#_Toc405320461)

[8 Simulation 12](#_Toc405320462)

[8.1 Simulation results 12](#_Toc405320463)

[8.1.1 Transient analysis 12](#_Toc405320464)

[8.1.2 Bode plot (Amplitude and Phase) 12](#_Toc405320465)

[9 List of Figures 13](#_Toc405320466)

# Tasks

The aim of this lab exercise was to build a broadband amplifier. All values were calculated and the broadband amplifier were dimensioned in all his circumstances, at last and finally the hole circuit was implemented and built.

The following tasks were achieved and implemented.

1. Dimensioning
2. Measuring construction
3. Measuring of the bias voltage
4. Measuring of output voltage
5. Bode diagram (amplitudes and phase response)

## Given Values

The following Values were part of the individual tasks achievement and were taken firstly for dimensioning.

# Dimensioning

For all following calculations the values from 2.1 Given Values were taken as reference.

The first assumption was that the voltage URE. The range of this voltage was 1-2 V.

Assumption: URE=2V

Based on this value the emitter resistor was calculated.

Afterwards the voltage UCE1 was chosen. The range for this voltage was 1-2.

Assumption: UCE1=2V

Finally the current for the voltage divider was assumpted. The supposition for this current was 20 times bigger than I. From this it follows that: .

The so calculated value was a very important ingredient of the resistor calculation which set the bias voltage.

## Calculation R3

## Calculation R2

## Calculation R1

## Calculation C1 und C2

## Calculation C3

## Calculation C4

# Measuring construction

Following cicuit shows the used measuring construction inclusive all dimensioned und calculated values.

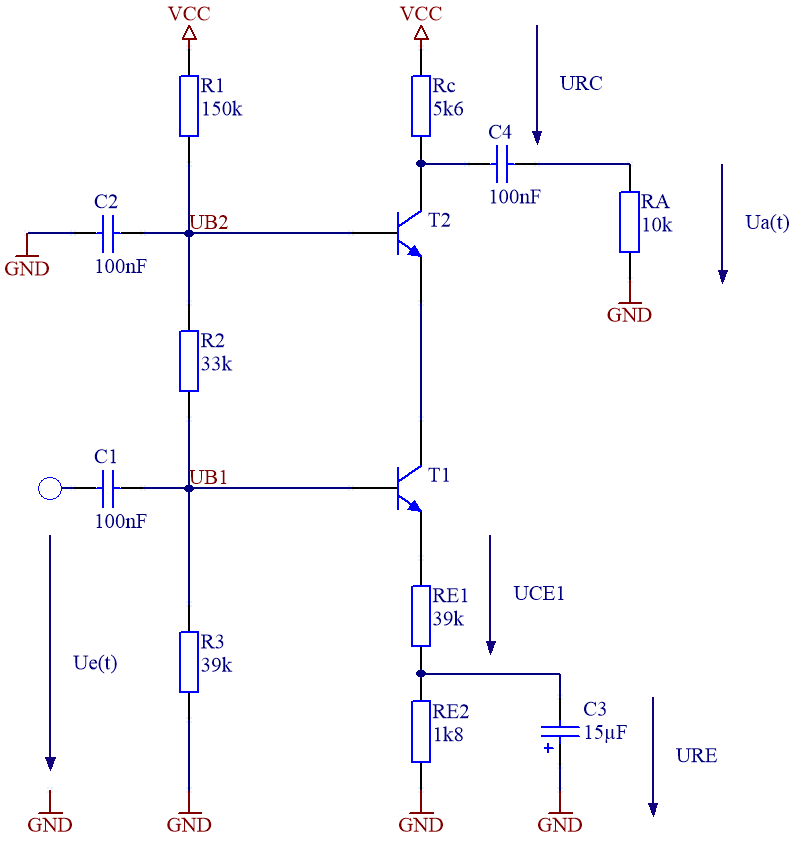


Figure 1. – Constructed measuring cicuit

# Bias voltage

The measurement of the bias voltage was made and compared with the calculated values.

The results of the measurement from the bias voltage were following.

|  |  |  |
| --- | --- | --- |
|  | Gemessen [V] | Berechnet [V] |
| UB1 | 2,56 | 2,7 |
| UB2 | 4,60 | 4,7 |
| URC | 5,80 | 5,5 |
| UCE1 | 2,10 | 2,0 |
| UCE2 | 5,05 | 5,5 |
| URE | 1,80 | 2,0 |

And additional measurement of the potentials UB1 and UB2 should exhibit in which range compared to the supply voltage the voltages were located.

On the measurement picture a GND and Vcc Line were drawn.



GND

Vcc

Figure 2. – Compare measurements

# Output measurement

A sinus with a frequency of 1kHz and an amplitude of 100mVpp was supplied on the input pun of the broad-band-amplifier. The following picture shows the input signal (Ue – Ch2) and the output signal (Ua – Ch1).

Based on this picture the gain could be read and also seen that the amplifier inverts the output signal.

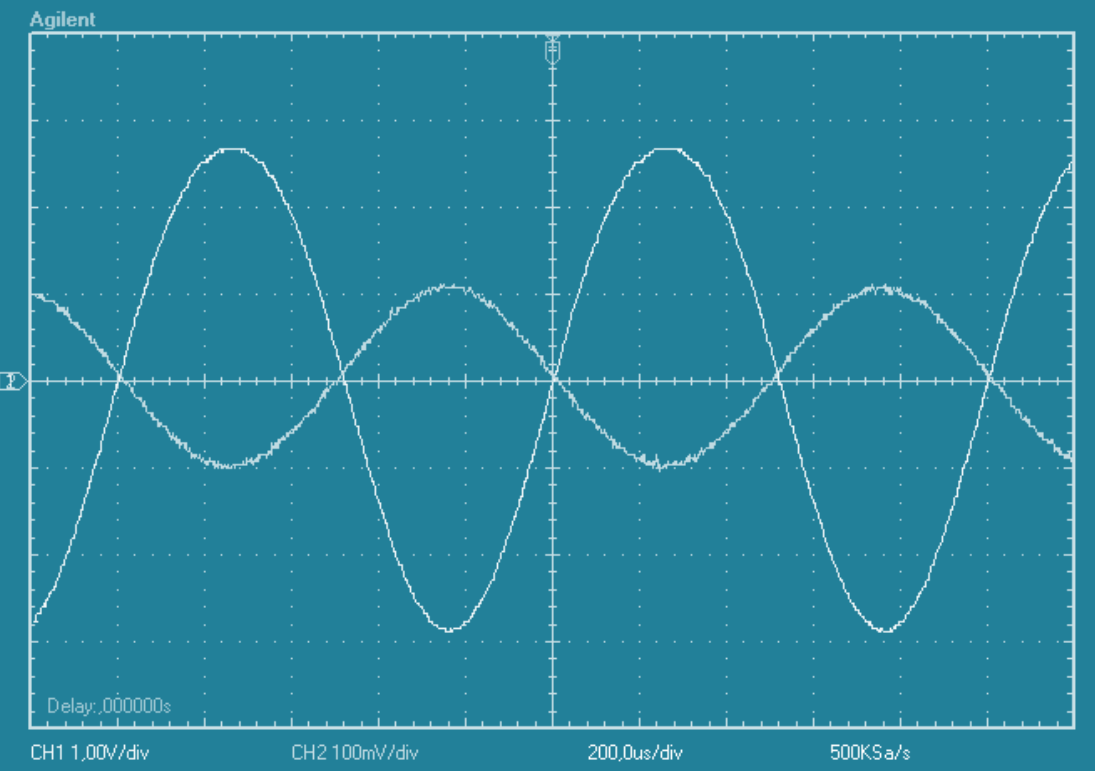


Figure 3. – Input signal with 100 mVpp and 1 kHz

## Dynamic Range

The following measurements showed the dynamic range of the amplifier.

By all following three signals an input signal of 1 kHz was used. The amplitude varies from 100mVpp to 300 mVpp.

Based on the following measurements it was recognizable that the amplifier works probably in the range of 1-100mVpp without saturation.

### Measurement results (100mVpp)

The first measurement was executed with an input signal with an amplitude from 100mVp.

The Signal is not much damped and available in it´s origin form.



GND

Figure 4. – Dynamic range (100mVpp)

### Measurement results (200mVpp)

The first measurement was executed with an input signal with an amplitude from 200mVp.

The output signal is already not very symmetric and a little bit cut off.



GND

Figure 5. – Dynamic range (200mVpp)

### Measurement results (300mVpp)

The first measurement was executed with an input signal with an amplitude from 300mVp.

In this measurement the amplifier is already in a heavy saturation.



GND

Figure 6. – Dynamic range (300mVpp)

# Bode plot

## Amplitude plot

Figure 7. – Measured Amplitude Response

## Phase plot

Figure 8. – Measured Phase Response

# Simulation

To consolidate the measured and calculated circuit the whole circuit was in addition to the built up simulated in Micro-Cap.

## Simulation results

### Transient analysis

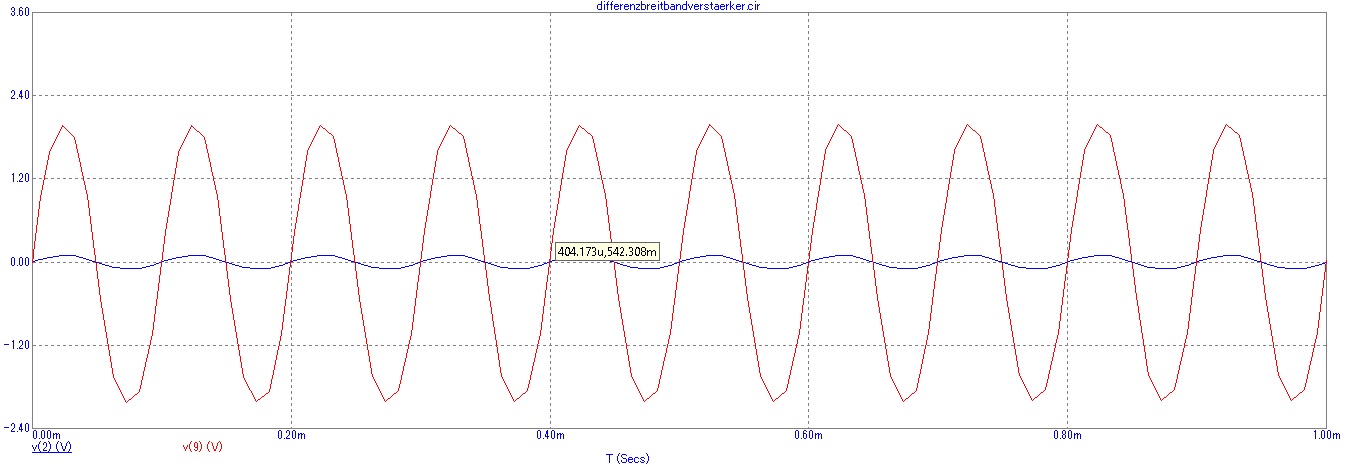


Figure 9. – Simulation of the Transient analysis with Micro-Cap

### Bode plot (Amplitude and Phase)

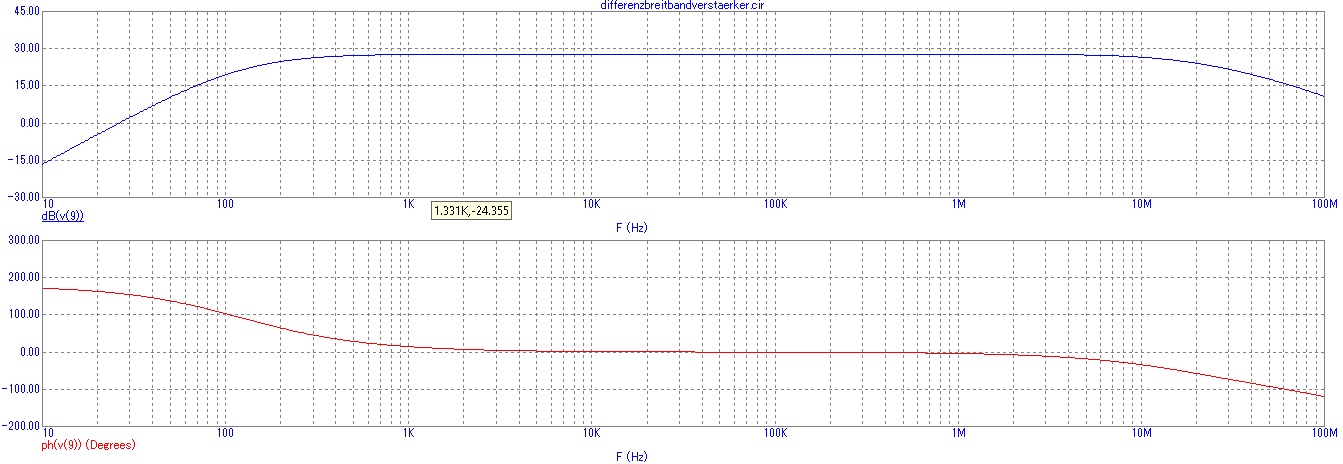


Figure 10. – Simulation of the bode plot with Micro-Cap

# List of Figures

[Figure 1. – Constructed measuring cicuit 5](#_Toc405358171)

[Figure 2. – Compare measurements 6](#_Toc405358172)

[Figure 3. – Input signal with 100 mVpp and 1 kHz 7](#_Toc405358173)

[Figure 4. – Dynamic range (100mVpp) 8](#_Toc405358174)

[Figure 5. – Dynamic range (200mVpp) 9](#_Toc405358175)

[Figure 6. – Dynamic range (300mVpp) 10](#_Toc405358176)

[Figure 7. – Measured Amplitude Response 11](#_Toc405358177)

[Figure 8. – Measured Phase Response 11](#_Toc405358178)

[Figure 9. – Simulation of the Transient analysis with Micro-Cap 12](#_Toc405358179)

[Figure 10. – Simulation of the bode plot with Micro-Cap 12](#_Toc405358180)